



Prof. Viond S. Mahajan

Department of Computer Engineering

CONTACT

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- [LinkedIn](#)

EDUCATION

- Ph. D (CSE) Pursuing- K. B. C. – NMU Jalgaon
- M. Tech (CSE)- RGPV – Bhopal
- B. E. (Computer) - KBC N M U Jalgaon

RESEARCH

- [Google Scholar](#)
- [Orcid](#)
- [Vidwan](#)
- [Resear Gate](#)
- [ResearchID](#)

MEMBERSHIP IN PROFESSIONAL BODIES

- Member of IAE
- Member of CSTA
- Member of IACSIT
- Member of ICST

BOOKS PUBLISHED

- 01

WORK EXPERIENCE

Professor in Computer Engineering with 19 years of experience in teaching, research, and academic administration. Recognized for dedication, strong academic background, administrative expertise, and a logical approach to problem-solving, with a sustained commitment to lifelong learning and alignment with the vision and mission of the institute.

RESPONSIBILITIES

- Head of Department
- IQAC Coordinator
- Member of Discipline Committee.

SUBJECT EXPERTISE

- Machine Learning
- Cryptography
- Object Oriented Programming

ACHIEVEMENTS

- NPTEL Star Award.

PAPERS PUBLISHED

	International	National
<i>Published in Journals</i>	5	0
<i>Presented in Conferences</i>	2	2

PATENTS/IPRS/ GRANT RECEIVED

Patent :

- A Title of Invention: Design Of BCD Adder using Five Variable KMap
- Application No. 202121027595
- Published Date: 27/08/2021