

RESUME

Mr. VIJAY BHAGAWAN NERKAR
M.Tech(Digital Communication)

Mobile:-9763683284

Date of Birth: - 09th August 1986

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Objective:

As an experienced teacher in an educational organization would be to render my knowledge, experience, and expertise in all aspects I would like to develop fascinating, and unique practices of teaching that creates interest in the Student

Educational Qualification:

Examination	Year	Stream	Board/University	Percentage
SSC	2001	---	Nasik	61.00
HSC	2003	Science	Nasik	62.00
B.E.	2008	Electronics & Telecommunication	North Maharashtra University, Jalgaon	66.91
M.Tech	2014	Digital Communication	Rajiv Gandhi Technical University, Bhopal	70.00

Teaching Experience:

Duration	Year	Employer
9 yrs	21 July 2008 to till date	P.S.G.V.P.Mandal's D.N.Patel COE Shahada

University Approval No: NMU/18/935/2009 (Date: 29.08.2009)

M.Tech. DISSERTATION DETAILS.

Dissertation Title : "A Secure Skin Tone Based Steganography Using Double Density Discrete Wavelet Transform".

Skills:

- Dedicated towards assigned tasks.
- Belief in teamwork.
- Effective Teaching.

Publication:

- “A Secure Skin Tone Based Steganography Using Double Density Discrete Wavelet Transform “International Journal of Electrical, Electronics and Computer Engineering 2(2):83-90(2013) ISSN NO (online)2277-2626
- “Early Pest Identification in Agricultural Crops Using Image Processing Techniques” International Journal of Electrical, Electronics and Computer Engineering 2(2):77-82(2013) ISSN NO (online)2277-2626
- “SCM for Broadband Wireless Systems “International Journal on Emerging Technologies 2(2):34-39(2011) ISSN NO (Print):0975-8364 ISSN NO.(Online):2249-3255
- “ECG Data Compression Using DWT” International Journal of Engineering and Advanced Technology (IJEAT) ISSN2249-8958,volume-1,Issue-1,oct-2011

Academic Taught Subjects:

- Analog & Digital Electronics
- Digital Techniques & Application
- Radiation Microwave & Techniques
- Network & lines
- Elements of Electrical & Electronics Engineering
- Power Electronics
- Industrial Economics & Telecom Regulation
- Industrial Management
- Linear Integrated Circuits
- Component Devices & Instrumentation Technology

Other Responsibilities (University Level) :

- Examiner for Oral/Practical Examination.
- University paper assessment.

- University paper Setter.

Other Responsibilities (Department Level) :

- Class Teacher of BE (E &TC) (2012-13), SE (E&TC) (2015-16 to 2016-17)
- Test Incharge (2014-15)
- T&P Incharge (2010-11 to 2015-16)

Permanent Address : 13, Vidhya Vihar Colony, Old Mohida road
Shahada, Tal. Shahada, Dist. Nandurbar-425409 (MH)

Nationality : Indian

Languages Known : English, Hindi and Marathi.

DECLARATION:-

I hereby declare that the above written particulars are true to the best of my knowledge and belief.

Place: Shahada

Date:

Yours faithfully

Nerkar Vijay Bhagawan