

## *CURRICULAM VITAE*

**PATIL NANDKISHOR CHHAGAN**

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**Objective**

A decisive role in growing the institution where optimum utilization of my knowledge for encompassing execution of various complex technologies that can give tangible benefit to the institution. Accomplished, skilled and talented candidate with diverse knowledge of imparting knowledge in the areas of Electronics and Communication Engineering.

**Summary**

- Ability to generate effective teaching by making use of various methodologies
- Possess eight years of professional experience in teaching Electronics and Telecommunication subjects
- In-depth knowledge of Fiber Optic Communication, Analog and Digital Electronics, Satellite Communication and Digital Communication Engineering.
- Ability to teach in a multiethnic and multicultural environment
- Possess good time management and organizational skills
- Ability to carry out tasks with minimal supervision with positive attitude

**Academic  
Credentials**

**Master of Engineering (Electronics)** from Government College of Engineering, Aurangabad,  
An autonomous institute of Govt. of Maharashtra, India  
**Aggregate Percentage:83.13%      CGPA: 9.063/10**

**Bachelor of Engineering (E&TC)** June 2004 from D.N.Patel College of Engineering, Shahada  
North Maharashtra University, Jalgaon , Maharashtra, India  
**Percentage: 72.70%      Aggregate: 61.65%**

**H.S.C.** June 2000 from G.T.P. College, Nandurbar,  
Nasik Board, Maharashtra, India  
**Percentage: 69.00%**

**S.S.C.** June 1998 from S.A.M. English Medium School, Nandurbar,

Nasik board, Maharashtra, India

**Percentage: 73.20%**

<b>Experience</b>	<p>Serving as an <b>Approved Assistant Professor</b> in D.N.Patel College of Engineering, Shahada, Dist: Nandurbar, Maharashtra, India since 02 August 2004 to till date.</p> <ul style="list-style-type: none"><li>• Responsible for teaching concepts of Digital Signal Processing, Fiber Optic Communications, Satellite Communication, Digital Communications, VLSI Design and Analog and Digital Electronics</li><li>• presentations in areas of Digital Data coding using FPGA, wireless sensor network, Vishing an network security issue,</li><li>• Assessed and evaluated the performance of students on monthly basis</li></ul>
<b>Subject taught</b>	<p>Fiber Optic Communication , Satellite Communication , Satellite And Mobile Communication , Digital Signal Processing , Elements Of Electrical And Electronics Engineering , Advanced Light Wave Communication</p>
<b>Academic projects and seminars</b>	<p><b>Under graduation</b> Project - <b>‘Smart Card Security for Lockers’</b> Seminar - <b>‘Conditional Access System’</b></p> <p><b>Post graduation</b> Seminar I - <b>‘Bluetooth Based Wireless Sensor Network’</b> Seminar II - <b>‘VISHING’ An Emerging Attack Vector</b> Dissertation- <b>‘FPGA based Efficient Implementation of Viterbi Decoder’</b></p>
<b>Curricular activities:</b>	<ul style="list-style-type: none"><li>• Paper published in International journal : <b>Twelve</b></li><li>• International Level Paper Presentation : <b>Two</b></li><li>• National Level Paper Presentation : <b>Six</b></li><li>• Certified with “Class-A Proficiency Award” for completing TBT2016 by <b>IIT, Bombay.</b></li><li>• Participated in State Level seminar on ‘Recent Advancement in Nanocrystalline Material and Applications’.</li><li>• Participated in workshop on ‘Research Methodology’</li></ul>
<b>Personal details</b>	<ul style="list-style-type: none"><li>• Date of birth :- 8<sup>th</sup> June 1982</li><li>• Sex :- Male</li><li>• Nationality :- Indian</li><li>• Marital status :- Married</li><li>• Hobbies :- Music, Sports, Travel</li><li>• Languages known :- English, Hindi, Marathi and Gujrathi</li></ul>