

Resume

Name: Kiran Y. Chaudhari

Address: 66, Vidyavihar, M.T. Haveli,
Tal: Shahada, Dist: Nandurbar,
Pin: 425409. (M.S.)

Mobile No.: 9423226698

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Date of Birth: 28th April 1974.

Sex: Male. **Marital Status:** Married

Carrier Objective:

To earn quality experience through creative and sincere working.

Computer Proficiency:

Operating System : Windows XP, NT 4.0, Windows 95/96, DOS.
Office Automation Packages : Ms- Word, MS – Excel.
Languages : C, C++.
RDBMS : MS-SQL 7, MS access.
Web Designing Tools : Flash Player, Fireworks, Dreamweaver, Adobe Photoshop,
Hardware Platform : Intel Based

Employment Details:

1. From 1st July' 98 to 10th Feb' 99, worked with "Sigma Control Pvt. Ltd.", Goregaon (e), Mumbai, As Trainee Engineer.

Job Profile:

- Installing different types of controlling instruments (like temperature, flow, Level, Pressure Control loops, Conductivity and ph meters) in various industries.
 - Supervising of manufacturing process control systems and control panels.
 - Maintenance and calibration of all types of field instruments such as temperature, flow, Level, Pressure Control loops, Conductivity and ph meters in various industries.
 - Control panel designing.
2. From 15th Feb'99 to 25th Nov'99, worked with "Gangamai Collage of Engineering", Nagaon, Dhule, As a Lecturer in Instrumentation Dept.

Job Profile:

- Taught subject like, Transducers, Modern Control theory.
- Project Incharge.

3. Form 1st Dec'99 to 4th Aug'02, worked with "SvarSoft Pvt. Ltd.", Nigdi, Pune as Software Engineer.**Job Profile:**

- Programming in C, C++.
- Web page designing.
- Maintaining and Modifying Web sites.
- Designing Data bases for various automation systems.

4. From 1st Sept'02 to 5th Sept' 03, working with Delta Engineering, Baroda, as a Site Instrumentation Engineer.**Job Profile:**

- Planning for erection of all types of field instruments.
- Handling manpower at various sites.
- Attending Check list.
- Loop testing and calibration of all instruments.
- Technical support for instrument manufacturing division.

5. Since 10th Sept' 03 to 31st Aug '08 working as Lecturer and from 1st Sept'08 to till date working as Asst. Prof. with D. N. Patel COE., Shahada**Job Profile:**

- Taught subjects like, Transducers, Instrument System Design, Industrial Automation, Programmable Logic Controller & Distributed Control System, Analog Circuits & Analysis, Programming Practice-I, Programming Practice-II, Programming Practice-III, Microcontroller & Application , Microprocessor Etc.

Projects:

- Pay- Roll System.
- Hospital automation system
- Service center Automation system.
- School Automation system (Both in English and Marathi)

Websites Developed:

- www.lokmanyamedicalfoundation.org.
- www.supertechhospital.com
- www.marathilangna.com
- Developed Front Page for www.pcmcindia.org.
- www.svarsoft.com
- www.kalabiz.com

Academics:

- Passed **M.Tech. (Electronics & Instrumentation) specialization in VLSI & Microelectronics**, in 2008 From S.G.S.I.T.S, Indore (MP) with **69.38 % (First Division)**
- Passed **B.E. (Instrumentation)** in 1998 under North Maharashtra University, Jalgoan with **72.53% (1st class with distinction)**.
- Passed H.S.C. with 69.50% under Pune Board (1st Class)
- Passed S.S.C. with 74.00% under Pune Board.(1st Class)

Achievements:

1. Second rank merit certification in final year of B.E. from North Maharashtra University, Jalgoan.
2. Member of Board of Studies from 1st Jan 2004 to 1st Jan 2005.

Paper Presentations:

➤ **International Conferences**

1. Kiran Y. Chaudhari, D.S. Ajnar, N.J. Patil, Dipak P. Patil, “ Design of DCT Based JPEG Encoder and Decoder using VHDL”, Dehradun Institute of Technology , Dehradun, July 2007.
- 2.

➤ **National Conferences**

1. Kiran Y. Chaudhari, D.S. Ajnar, Dipak P. Patil, “Benefits of FPGA in wireless base station baseband processing application”, Proc. of RTIT 07, J. T. Mahajan College of Engineering, Faizpur (M.S.), pp. 56, Nov. 2007.
2. Kiran Y. Chaudhari, D.S. Ajnar, N.J. Patil, Dipak P. Patil, “Fuzzy Based Image Clustering and Compression”, Proc. of ICIT 07, NMIMS, Mumbai,pp.21, March 2007.
3. Kiran Y. Chaudhari, D.S. Ajnar, N.J. Patil, “Fuzzy Control System using VHDL”, Proc. of BITCON 07, Bhilai Institute of Technology, Durg, pp. 137-144, March 2007.
4. Kiran Y. Chaudhari, D.S. Ajnar, N.J. Patil ,“Implementation of DWT using VLSI for Image Compression”, Proc. of NCDIM 07, Thadomal Shahani Engineering College, Mumbai, pp. II 61, March 2007.
5. Kiran Y. Chaudhari, D.S. Ajnar, N.J. Patil ,“Image Enhancement using Fuzzy Logic”, Proc. of NCDIM 07, Thadomal Shahani Engineering College, Mumbai, pp. SP126-SP128, April 2007.

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